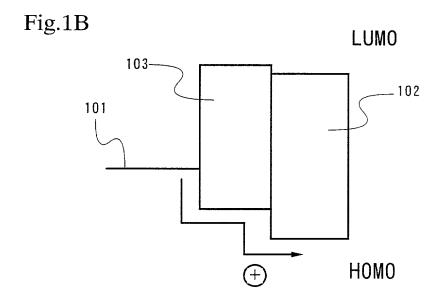
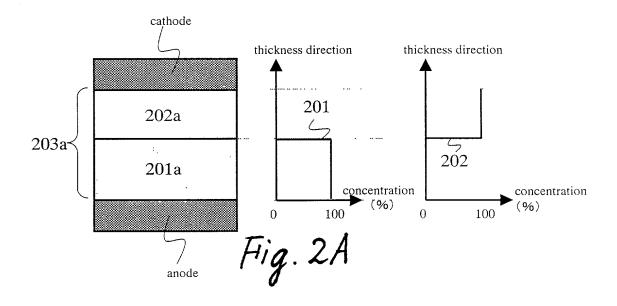
Fig.1A LUM0





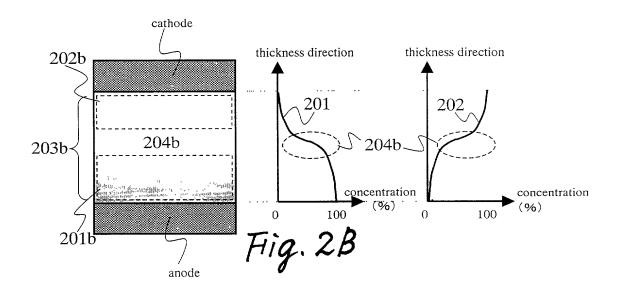


Fig.3A

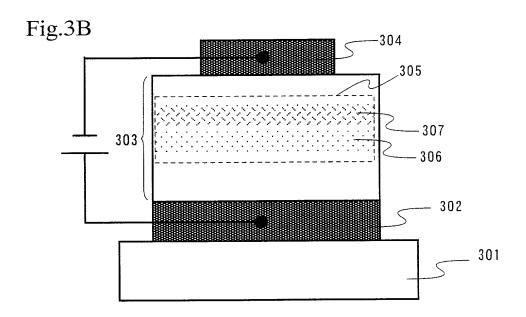
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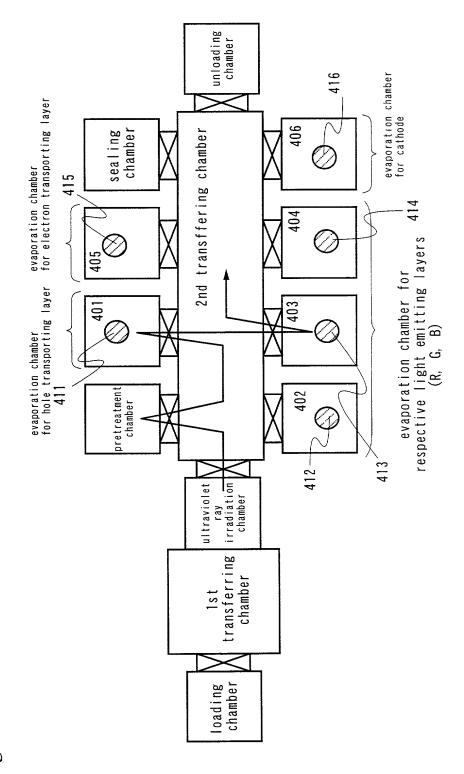


Fig.5A

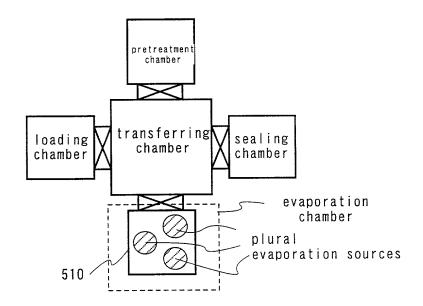
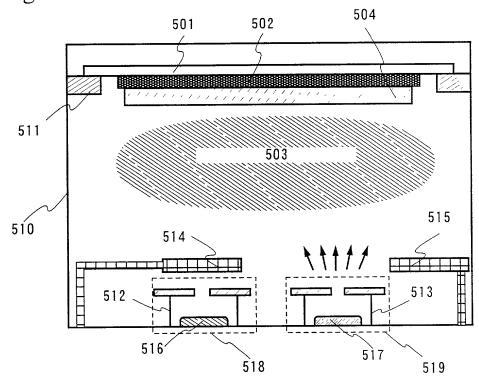


Fig.5B



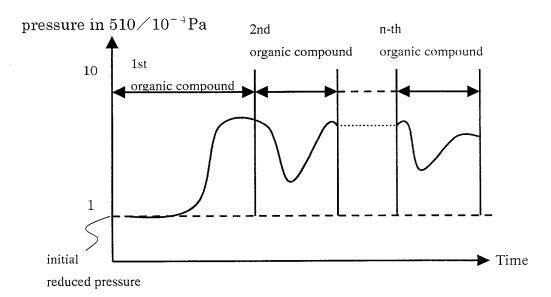


Fig.6A

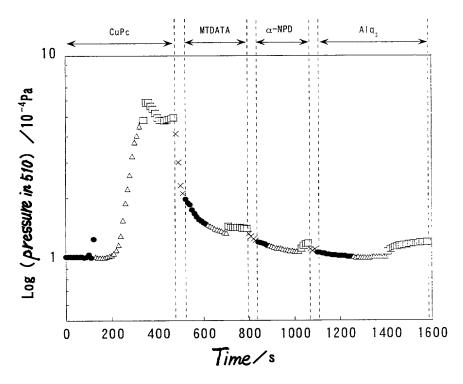


Fig.6B

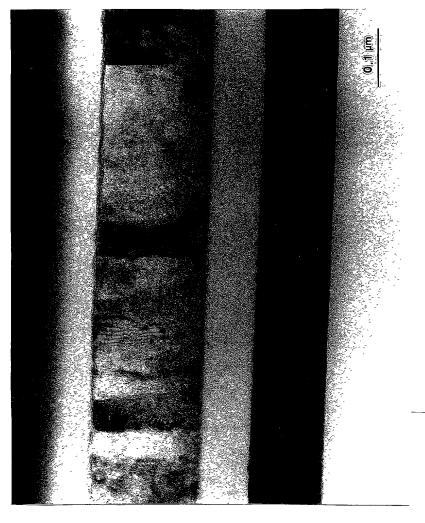


Fig. 7



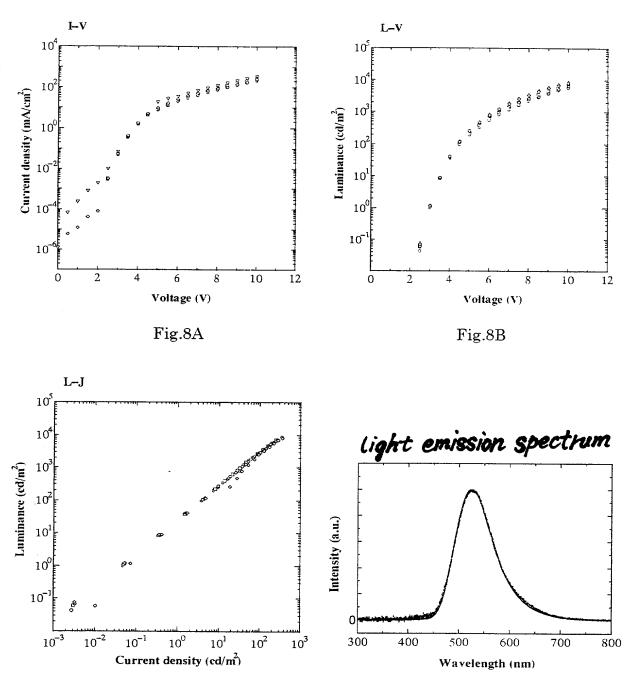


Fig.8C

Fig.8D

Fig.9

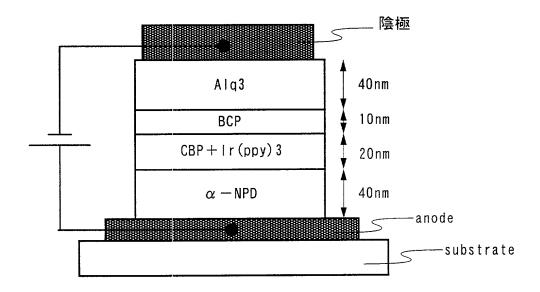


Fig.10A

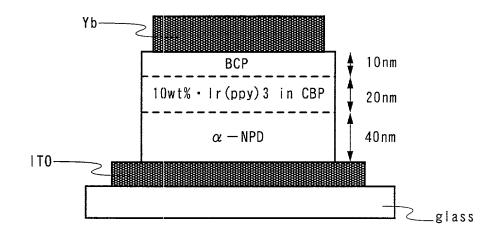
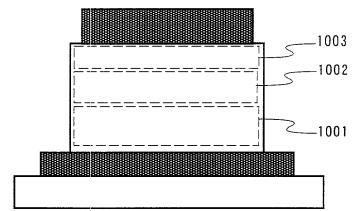


Fig.10B



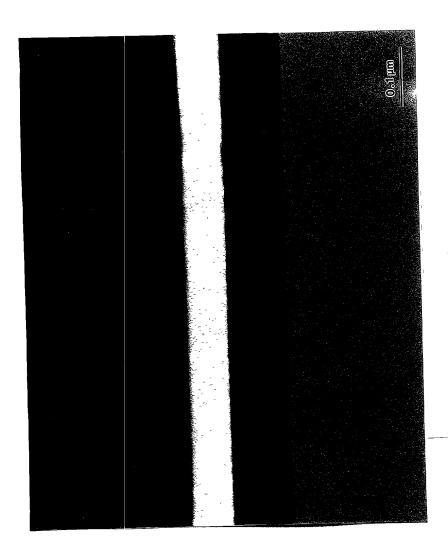
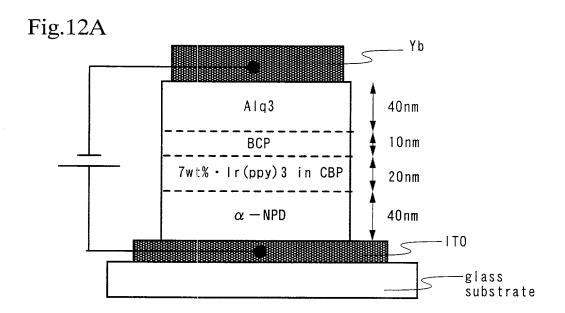
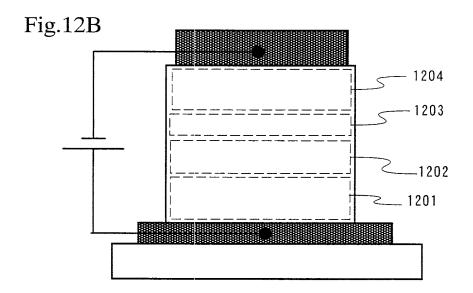


Fig. 11





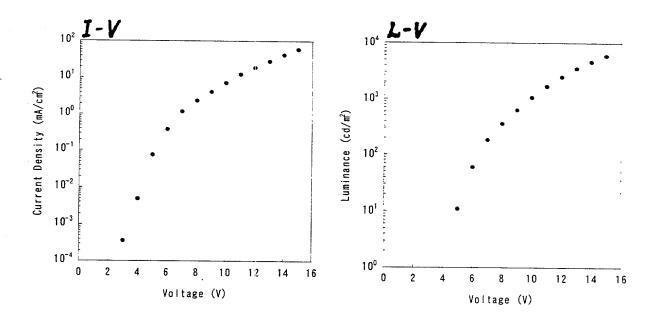


Fig.13A

Fig.13B

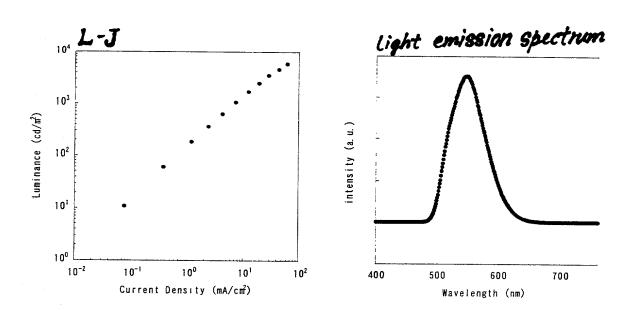
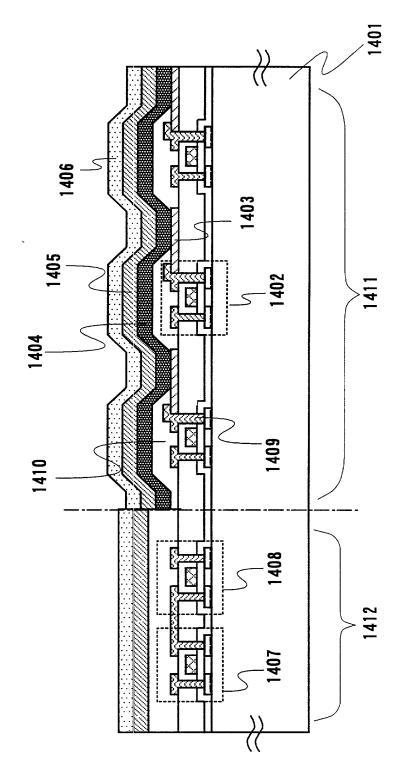


Fig.13C

Fig.13D





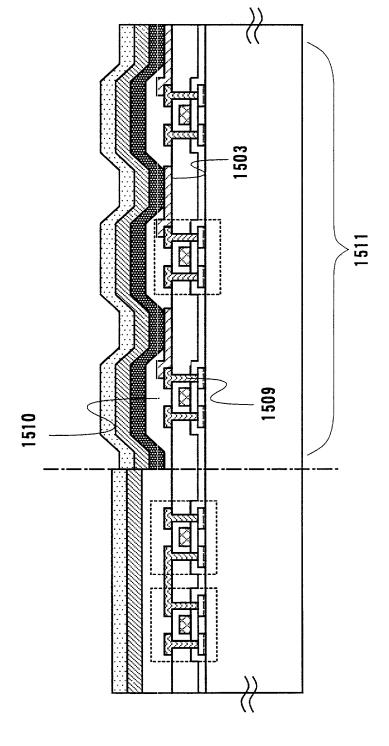
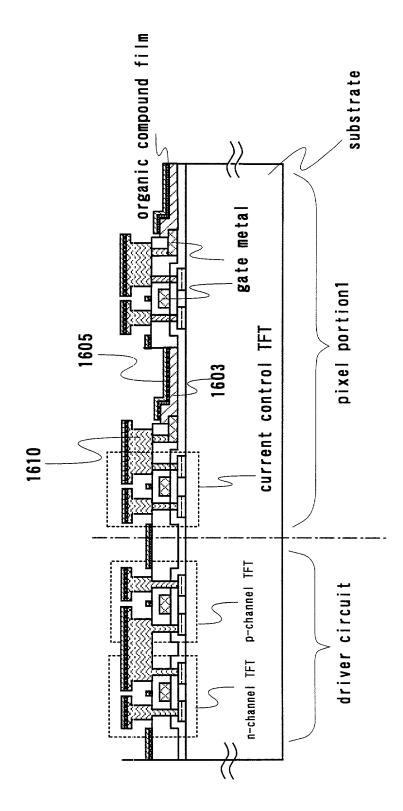
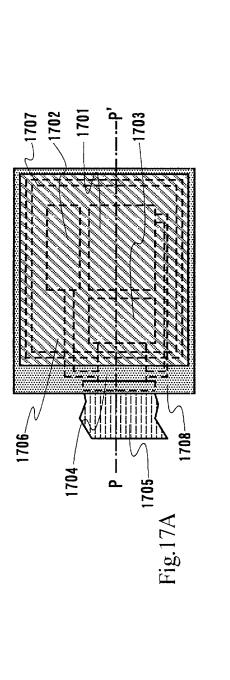
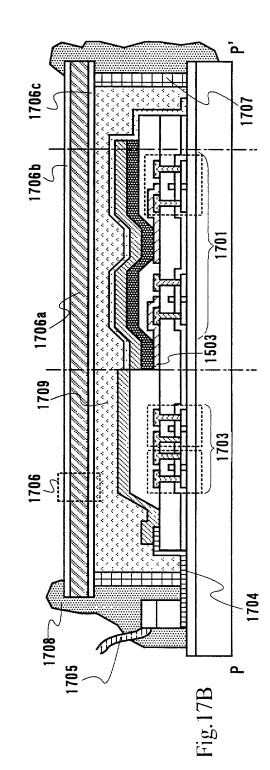


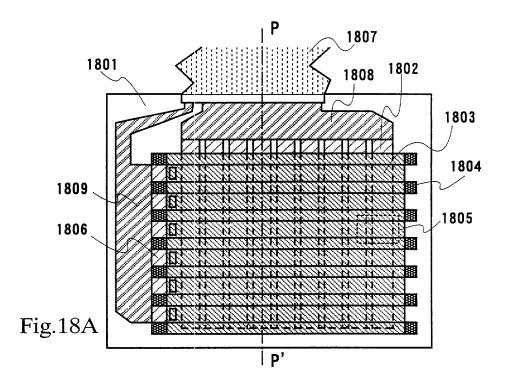
Fig.15

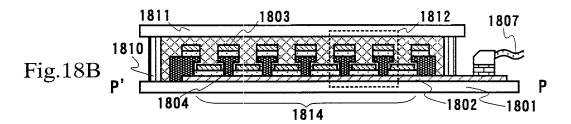


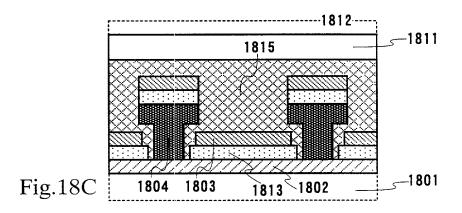












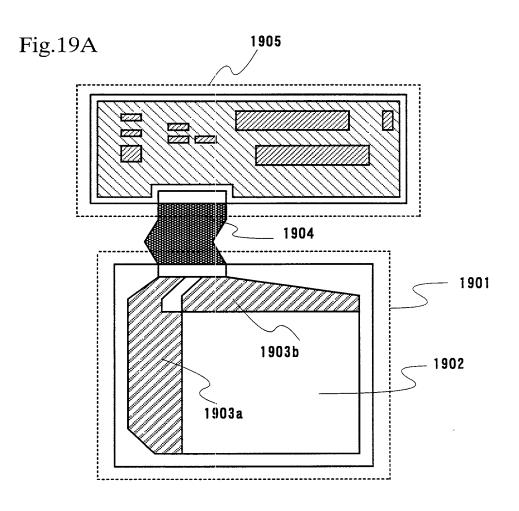


Fig.19B

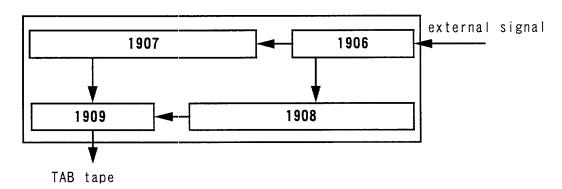


Fig.20A

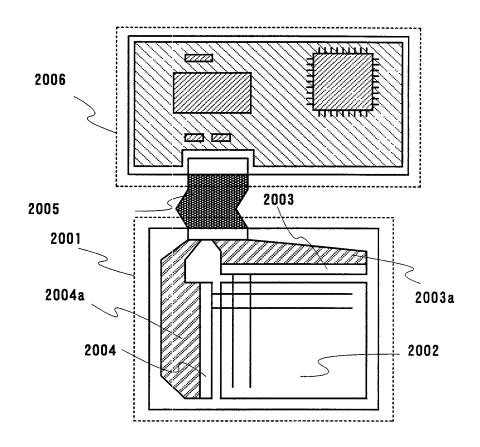
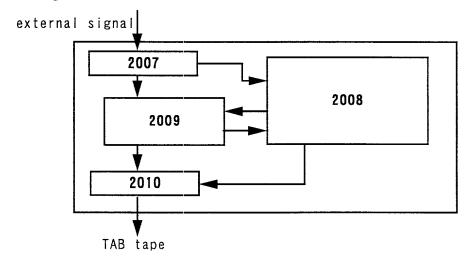


Fig.20B



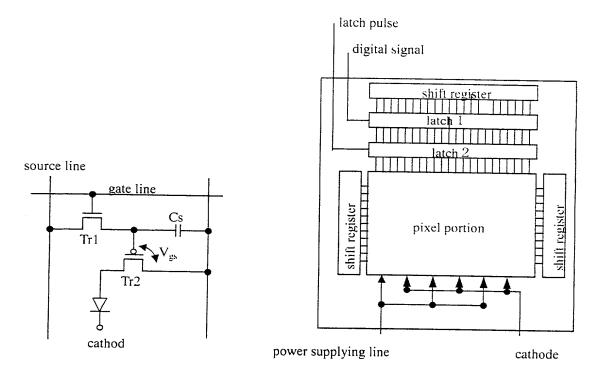
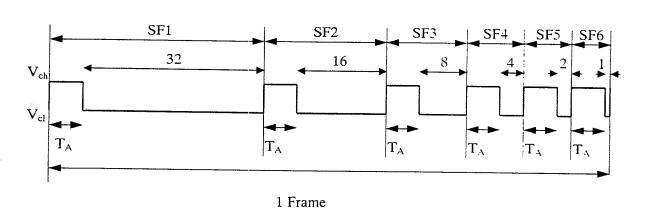


Fig.21A

Fig.21B



SF1-SF6:sub-frame, TA: write in time

Fig.21C

